

Diagram illustrating a cross-sectional view of a semiconductor device. The device is built on a substrate 50, which is a P+ region. A layer 55 is formed on the substrate, containing a P region. A series of P-N junctions are formed, with regions labeled 51, 52, 53, 54, 55, 30, 33, 35, N, N+, and P-.

FIG. 5.

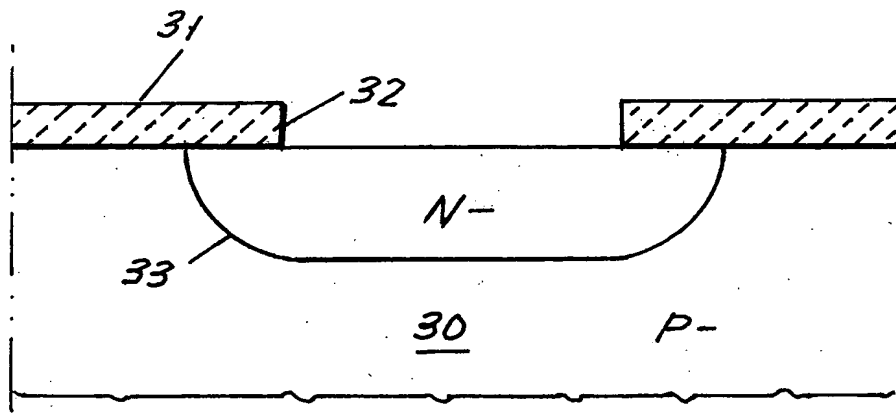


FIG. 2

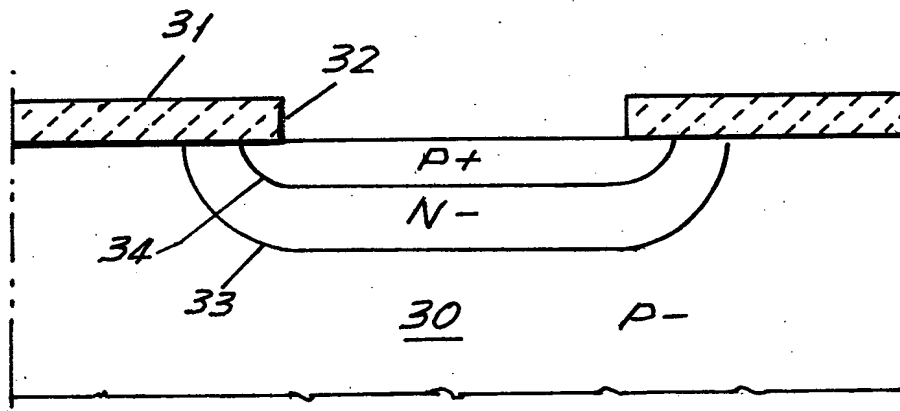


FIG. 3

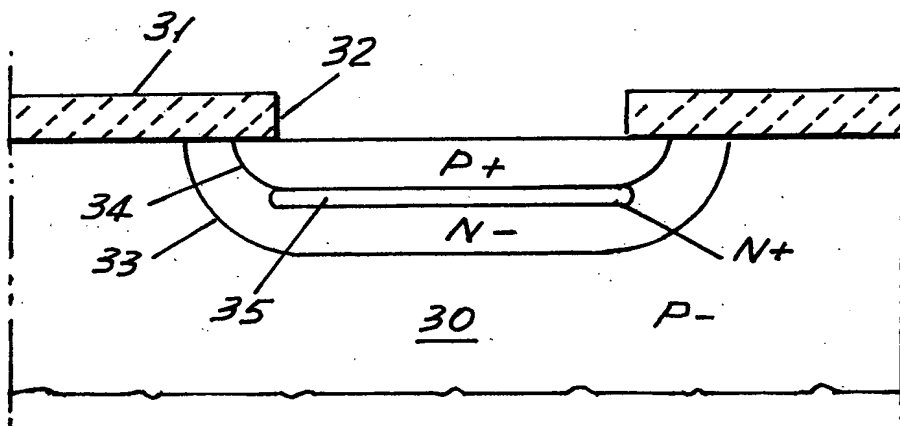


FIG. 4